DECEMBER 2024 **P/ID 16302/PIT1B**

Time : Three hours Maximum : 80 marks

PART A — (10 × 2 = 20 marks)

Answer any TEN questions.

1. Define register transfer.

2. What is address sequencing?

3. Expand RISC.

4. List the addressing modes.

5. Define ones compliment.

6. What is meant by register configuration?

7. Define control command.

8. What are the advantages of input output interface?

9. What is meant by key register?

10. What is the functionality of mapping process?

11. List out some datatypes.

12. Write short notes on floating arithmetic operations.

PART B — (5 × 6 = 30 marks)

Answer any FIVE questions.

13. Explain in detail about arithmetic logic shift unit with suitable example.

14. Explain about register stack in detail.

15. Draw a flowchart for floating point addition operation.

16. Elaborate input output processor with proper diagram.

17. Illustrate associative memory with example.

18. Explain in detail about registers for floating point arithmetic operations.

19. Write short notes on auxiliary memory.

PART C — ($3 \times 10 = 30$ marks)

Answer any THREE questions.

20. Elaborate register and bus transfer with neat diagram.

21. Write detail notes on stack organization with neat architecture.

22. Explain in detail about the steps involved in all arithmetic operation.

23. Illustrate the importance of serial communication.

24. Explain in detail about inter processor arbitration.

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